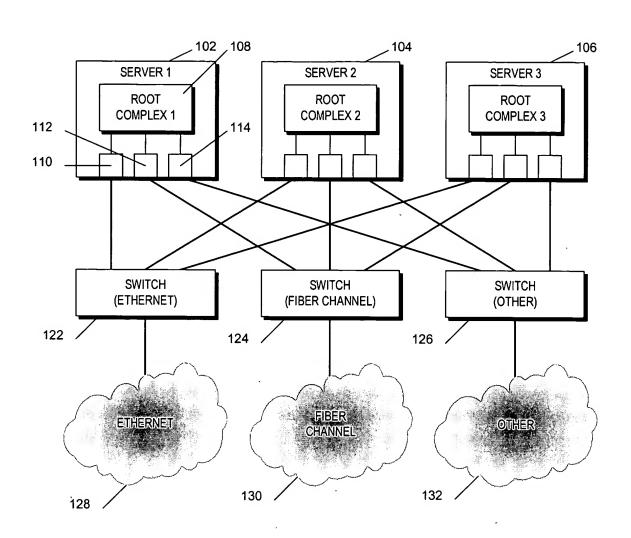
Fig. 1





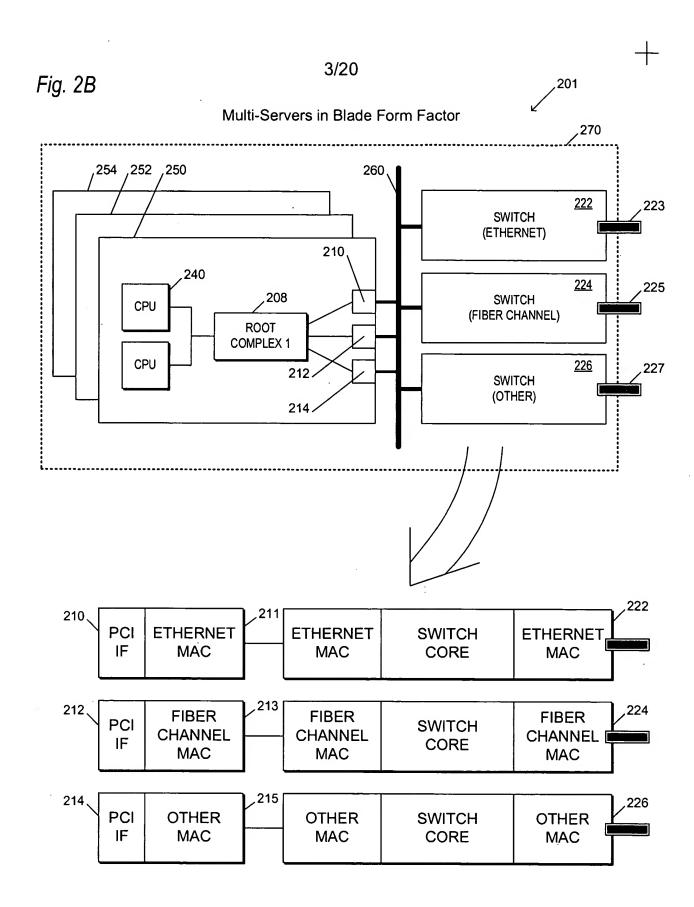


Fig. 2C

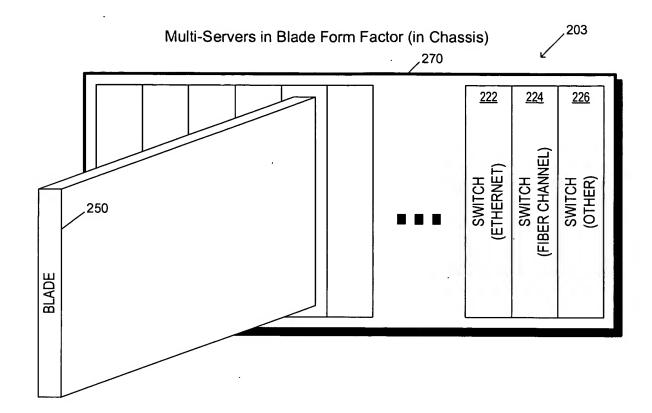
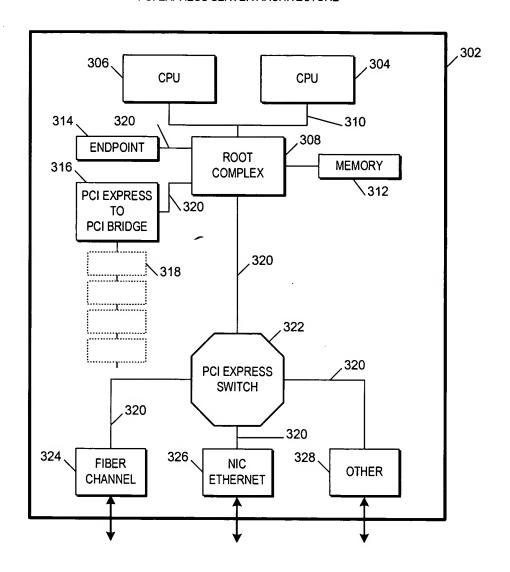
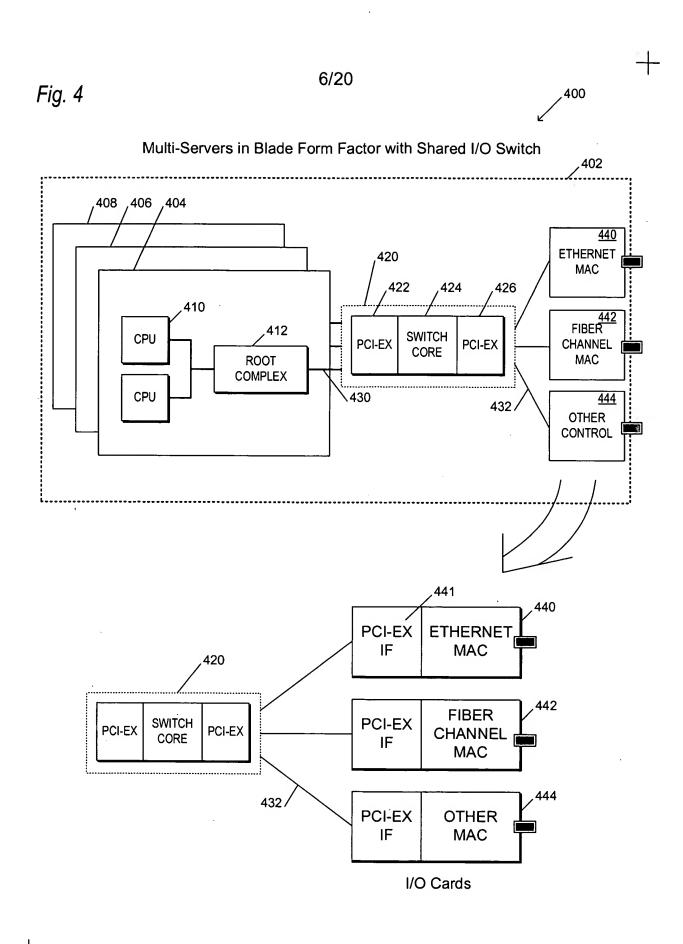


Fig. 3

300

PCI EXPRESS SERVER ARCHITECTURE





500

Fig. 5

MULTI-OPERATING SYSTEMS WITH SHARED I/O

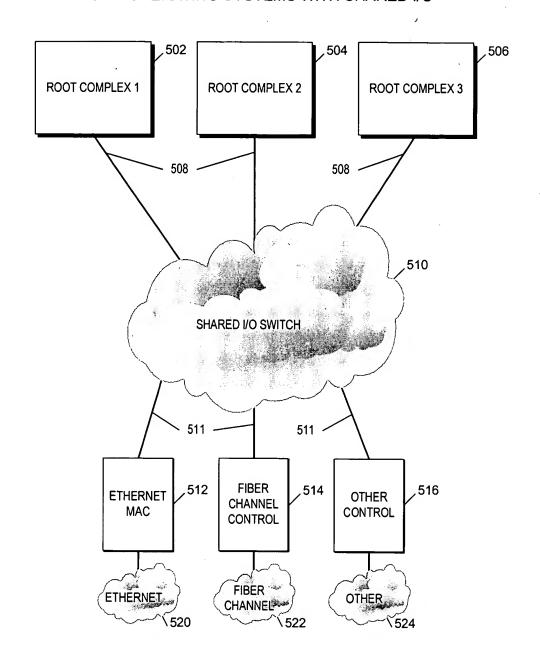


Fig. 6



MULTI-OPERATING SYSTEMS WITH SHARED ETHERNET CONTROLLER

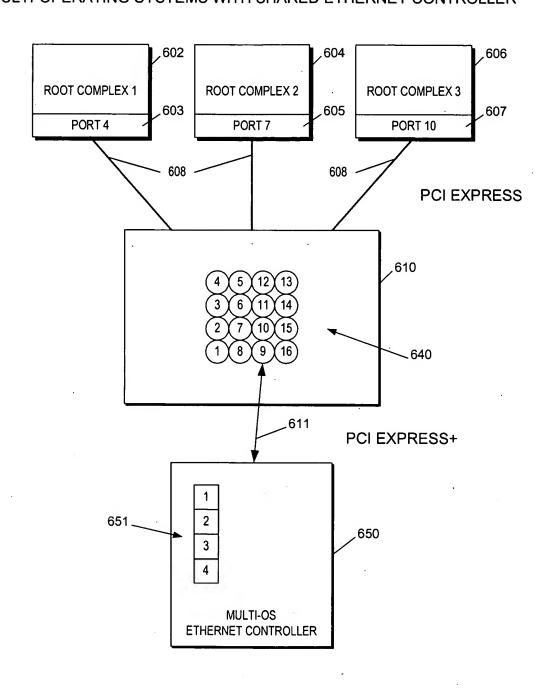


Fig. 7



MULTI-OPERATING SYSTEMS WITH SHARED FIBER CHANNEL CONTROLLER

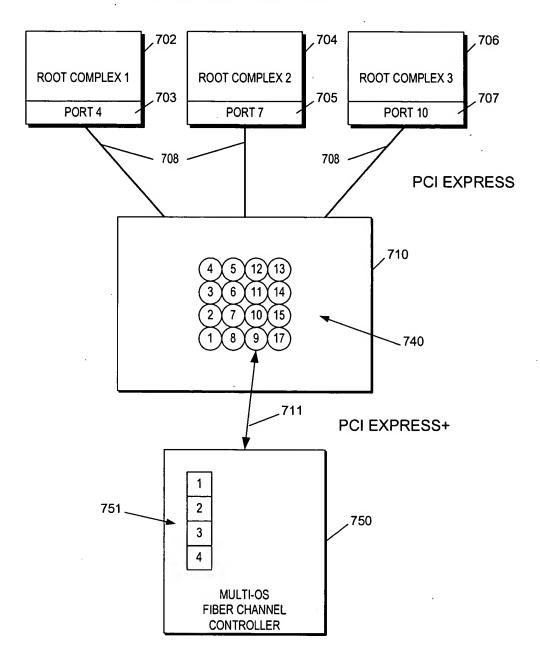
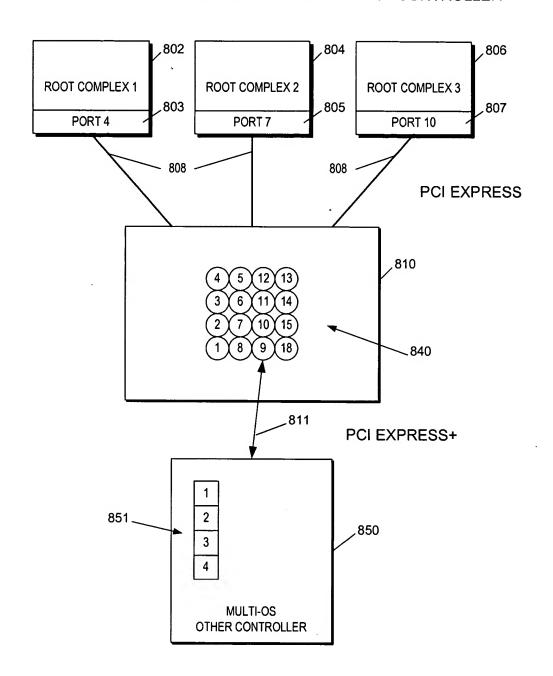
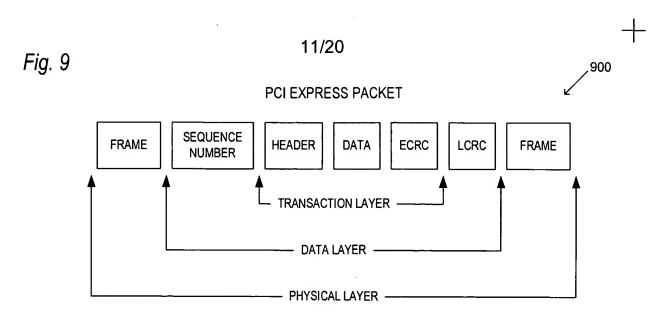


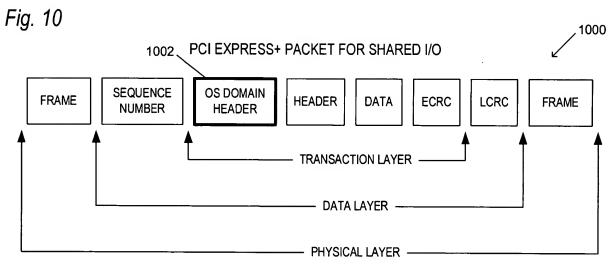
Fig. 8

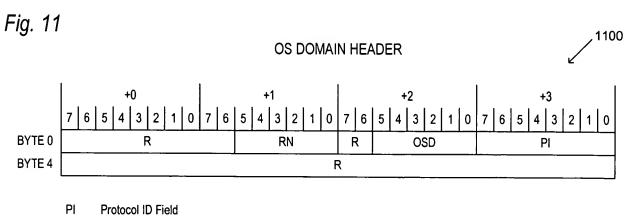


MULTI-OPERATING SYSTEMS WITH SHARED OTHER CONTROLLER







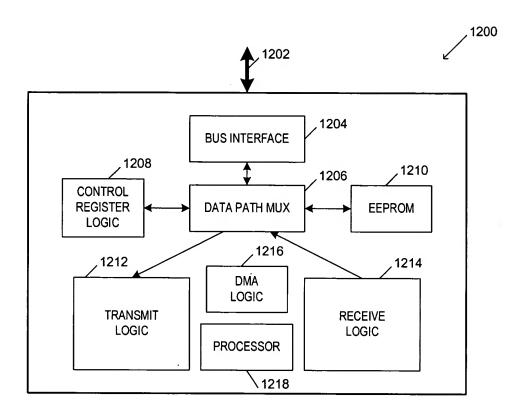


OSD OS Domain Number

RNResource Number (which buffer the packet belongs to)

R reserved

Fig. 12 (Prior art)



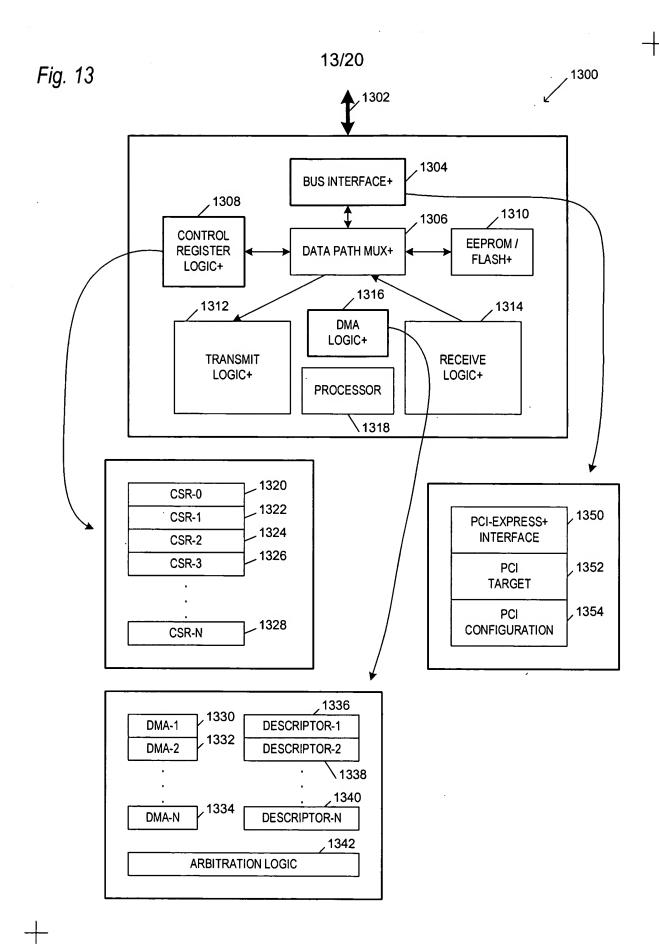


Fig. 14

1400

MULTI-OPERATING SYSTEMS WITH SHARED ETHERNET CONTROLLER PACKET FLOW EXAMPLE

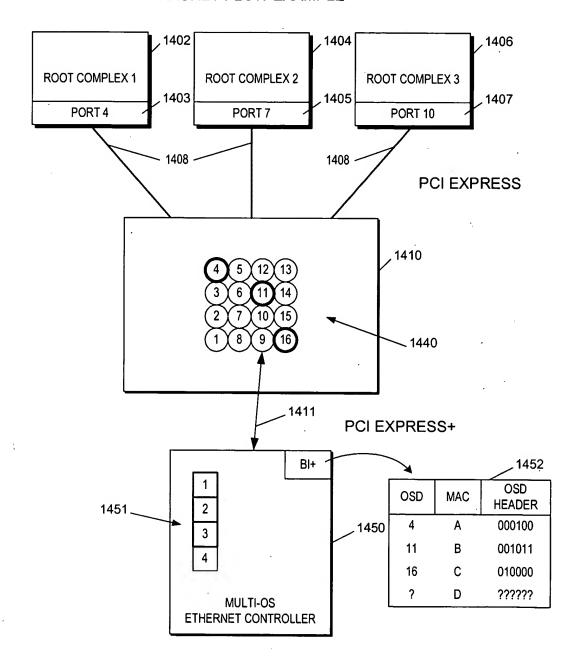


Fig. 15

{}1500 METHOD OF SHARED I/O DOWNSTREAM TRANSMISSION FROM SWITCH 1502 **BEGIN** .1504 N REQUEST FROM ROOT COMPLEX? 1506 **IDENTIFY DOWNSTREAM PORT** 1508 **BUILD PCI EXPRESS+ PACKET** [IDENTIFYING ROOT COMPLEX] 1510 SEND PCI EXPRESS+ PACKET TO ENDPOINT 1512 TRACK PROCESS FOR PCI EXPRESS+ PACKET 1514

DONE

Fig. 16

1600 METHOD OF SHARED I/O UPSTREAM TRANSMISSION TO SWITCH 1602 **BEGIN** 1604 Ν REQUEST FROM ENDPOINT? Υ 1606 IDENTIFY ROOT COMPLEX FROM PCI EXPRESS + PACKET 1608 BUILD PCI EXPRESS PACKET 1610 SEND PCI EXPRESS PACKET TO ROOT COMPLEX 1612 TRACK PROCESS FOR PCI EXPRESS PACKET .1614 DONE

Fig. 17

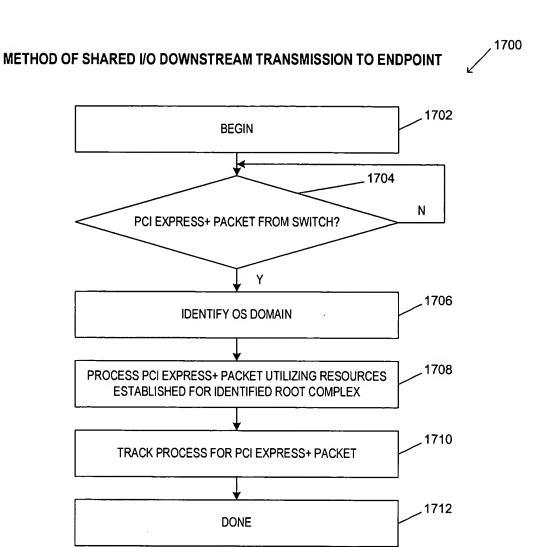
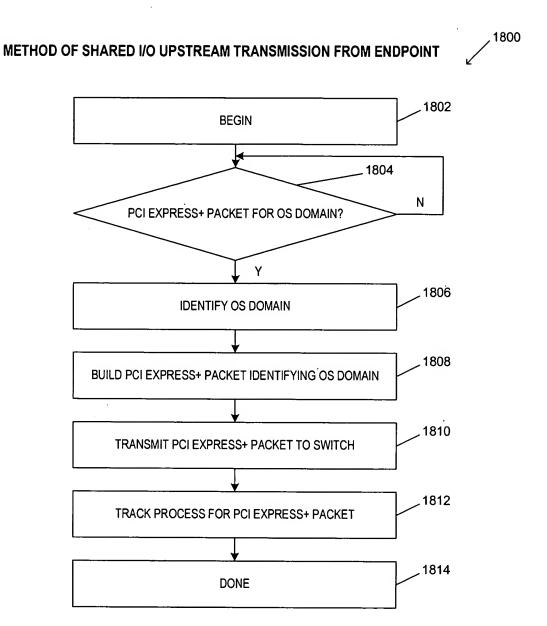
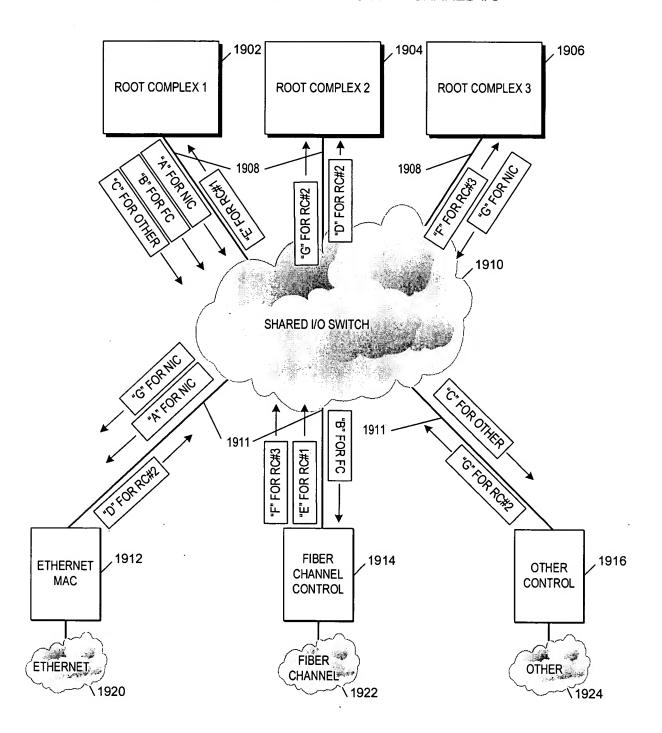
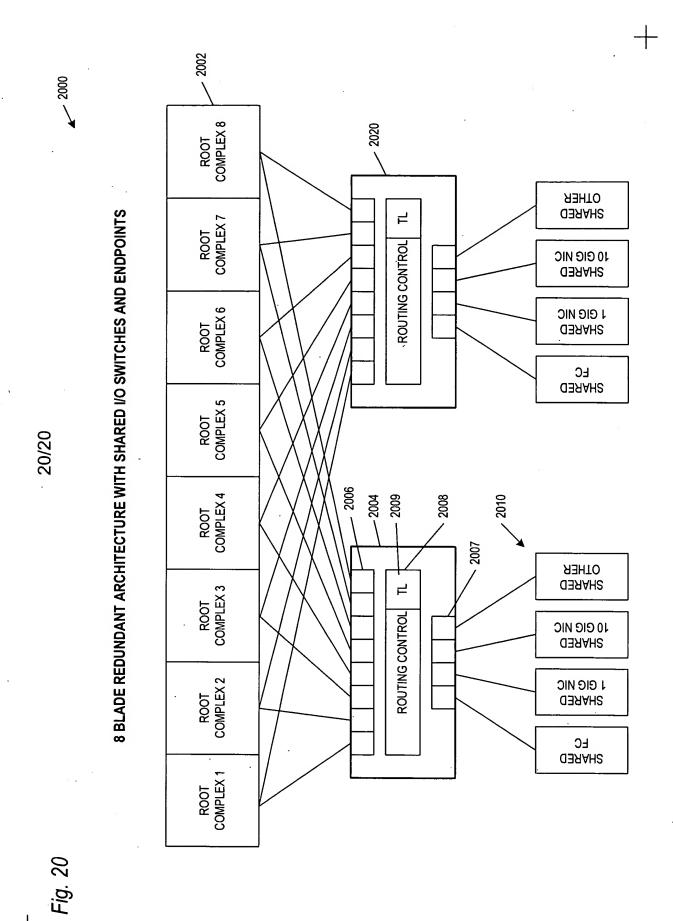


Fig. 18



MULTI-OPERATING SYSTEMS WITH SHARED I/O





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